Abstract

Provided is a gas discharge display panel that exhibits a favorable display performance by maintaining a wall charge retaining power, controlling discharge delay within a range adequate for optimal image display, and reducing the discharge starting voltage at comparatively low cost. Also provided is a PDP that exhibits more reliability with enhanced display quality by further improving the secondary electron emission factor γ compared to conventional cases and lowering the discharge starting voltage to widen the driving margin. Further provided is a manufacturing method of a gas discharge display panel, by which the manufacturing cost lowers by reduction of the exhaustion time in the sealing exhaustion process, and by which the driving circuit cost is reduced. In a gas discharge display panel of the present invention, a protective layer (15) has a first protective film (151) and a second protective film (152), the second protective film (152) is formed on at least part of a surface of the first protective film (151), and the first protective film (151) has a larger impurity content than the second protective film (152).

10

15